

SPECIFICATION

REFOND P/N

RF-WMRI32DS-FH-JZ

R&D

Mass Production



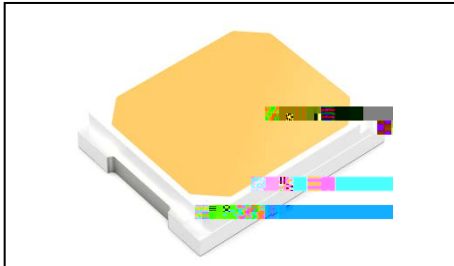
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1. Description

1.1



The yellow LED, which was fabricated by using a blue chip and the phosphor.

Product Package:2.80mmX3.50mmX0.70mm.

LED

2.80mmX3.50mmX0.70mm.

1.2 Features

PLCC2 Package.

Extremely wide viewing angle.

Suitable for all SMT assembly and solder process.

Available on tape and reel.

Moisture sensitivity level: Level 2.

Compliance with RoHS and REACH. RoHS REACH

Qualifications: The product qualification test plan is based on the guidelines of AEC-Q101
Stress Test Qualification for Automotive Grade Discrete Semiconductors :

AEC

1.3 Application

Automotive Lighting Interior and Exterior.



1.4 Package Dimension

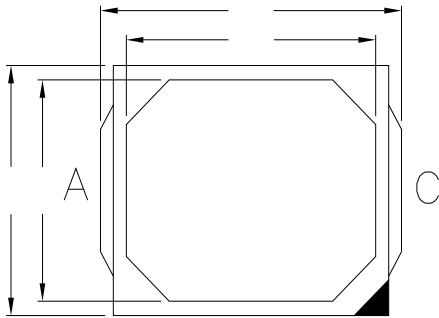


Fig.1-1 Top View

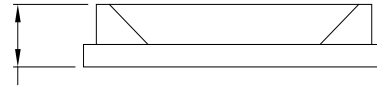


Fig.1-2 Side View

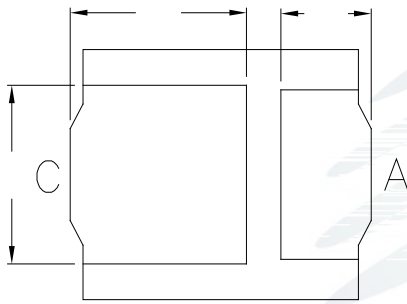


Fig.1-3 Bottom View

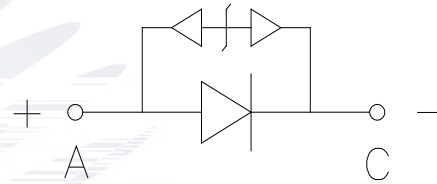


Fig.1-4 Polarity

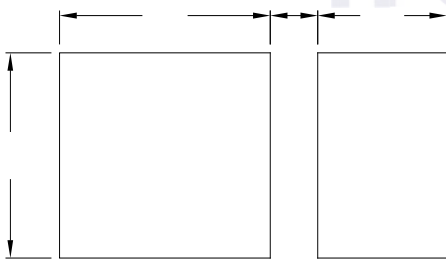


Fig.1-5 Soldering Patterns

Notes

1. All dimensions units are millimeters.
2. All dimensions tolerances are $\pm 0.2\text{mm}$ unless otherwise noted.



$\pm 0.$

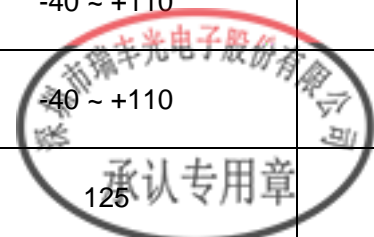
1.5 Product Parameters

Table 1-1 Electrical / Optical Characteristics at Ts=25°C

Item	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Forward Voltage	V_F	$I_F=150\text{mA}$	2.8	3.1	3.4	V
Reverse Current	I_R	$V_R=5\text{V}$	---	---	10	μA
Luminous Flux		$I_F=150\text{mA}$	37	40	50	lm
Viewing Angle		$I_F=150\text{mA}$	---	120	---	deg
Thermal Resistance.	R_{THJ-S}	$I_F=150\text{mA}$	---	---	23	$^{\circ}\text{C}/\text{W}$

Table 1-2 Absolute Maximum Ratings at Ts=25°C

Parameter	Symbol	Rating	Units
Power Dissipation	P_D	612	mW
Forward Current	I_F	180	mA
Peak Forward Current	I_{FP}	350	mA
Reverse Voltage	V_R	5	V
Electrostatic Discharge (HBM)	E_{SD}	8000	V
Operating Temperature	T_{OPR}	-40 ~ +110	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-40 ~ +110	$^{\circ}\text{C}$
Junction Temperature	T_J	125	$^{\circ}\text{C}$



Notes

- 1/10 Duty cycle, 10ms pulse width. ms, 1/10.
- The above forward voltage measurement allowance tolerance is $\pm 0.1V$. $\pm 0.1V$.
- The above color coordinates measurement allowance tolerance is ± 0.005 . ± 0.005 .
- The above luminous intensity measurement allowance tolerance $\pm 10\%$. $\pm 10\%$.
- Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
- All measurements were made under the standardized environment of Refond.
- When the LEDs are in operation the maximum current should be decided after measuring the package temperature, junction temperature should not exceed the maximum rate. LED
- ESD yield is over 90% at 200V ESD (HBM). ESD protection during products handing is needed. 90% LED ESD OOV ,

1.6 Bin Range Of Forward Voltage and Luminous Flux (IF=150mA)
BIN (IF=150mA)

Table 1-3

V_F	G1	G2	H1	H2	I1	I2
	2.8-2.9	2.9-3.0	3.0-3.1	3.1-3.2	3.2-3.3	3.3-3.4
Im	NA	NB	OA			
	37-40.9	40.9-45.3	45.3-50.0			



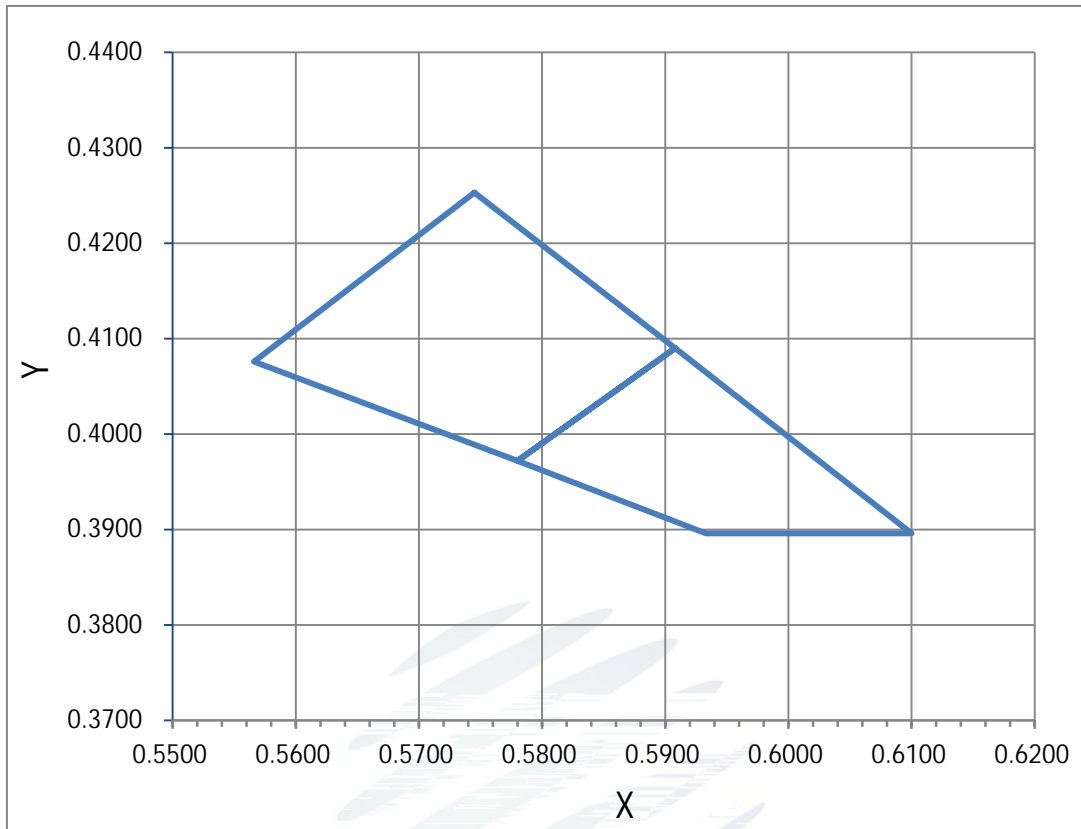


Fig 1-6 The C.I.E Chromaticity Diagram CIE

Table 1-4

BIN CODE	CIE-X1	CIE-Y1	CIE-X2	CIE-Y2	CIE-X3	CIE-Y3	CIE-X4	CIE-Y4
Y2	0.5745	0.4253	0.5566	0.4076	0.5780	0.3972	0.5908	0.4090
Y3	0.5908	0.4090	0.5780	0.3972	0.5933	0.3896	0.6100	0.3896



1.7 Typical Optical Characteristics Curves

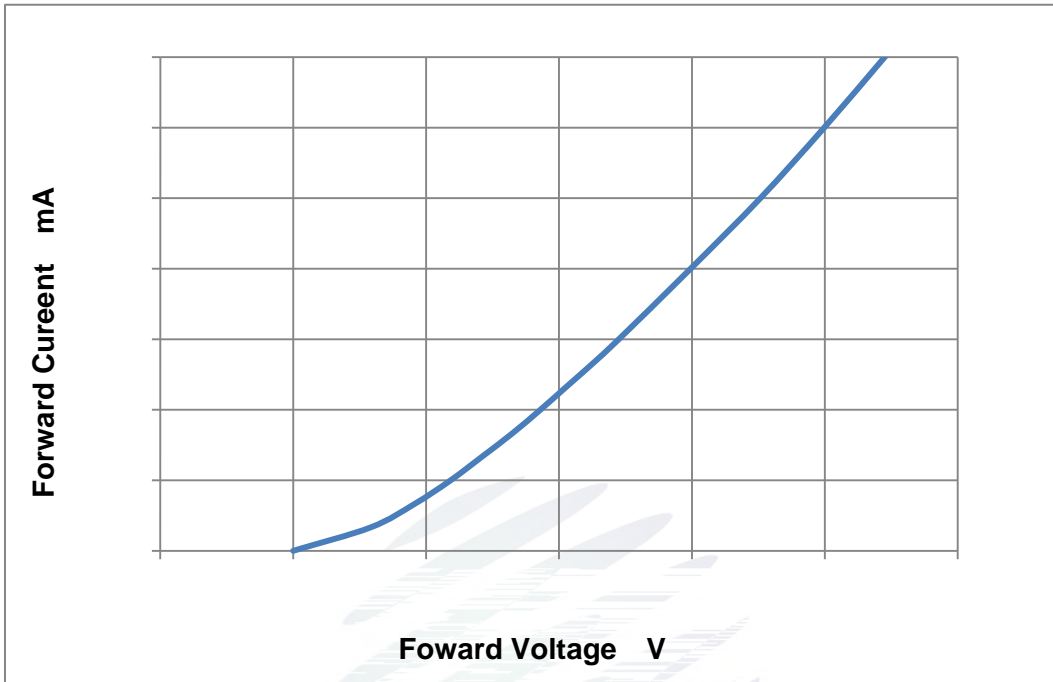


Fig. 1-7 Forward Voltage Vs Forward Current

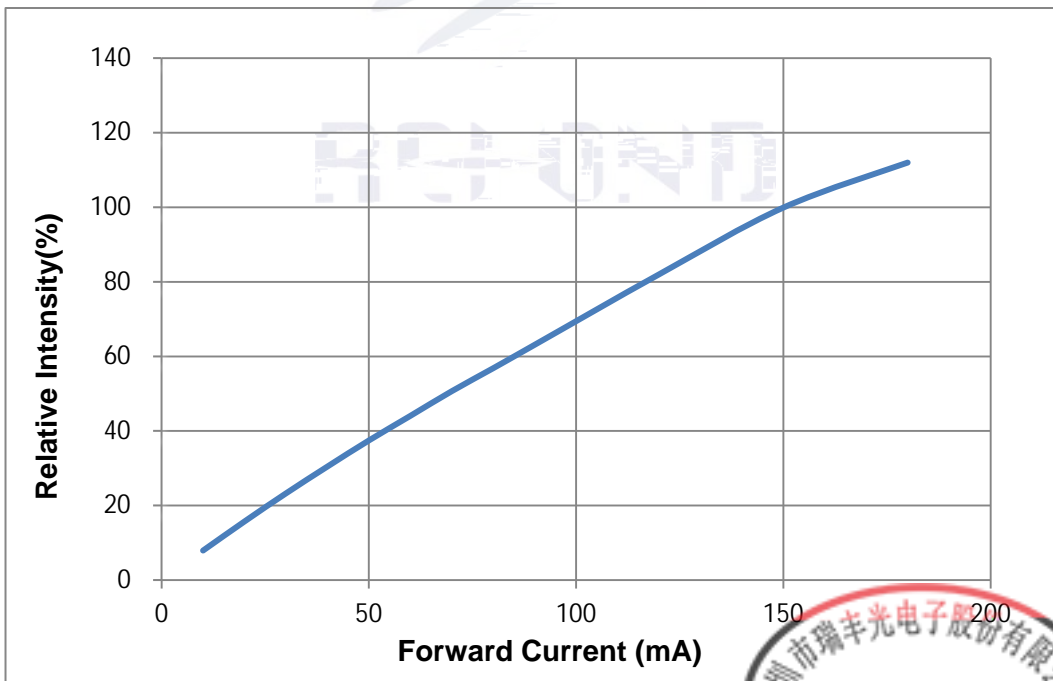
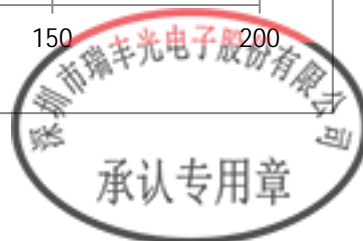


Fig. 1-8 Forward Current Vs Relative Intensity



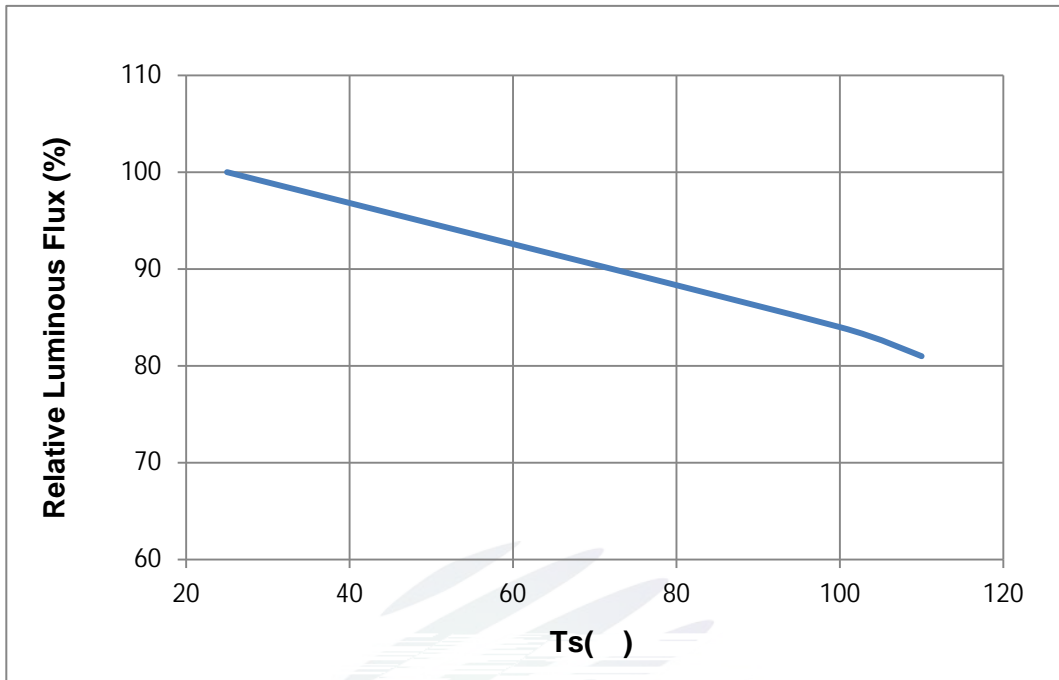


Fig. 1-9 Solder Temperature Vs Relative Intensity

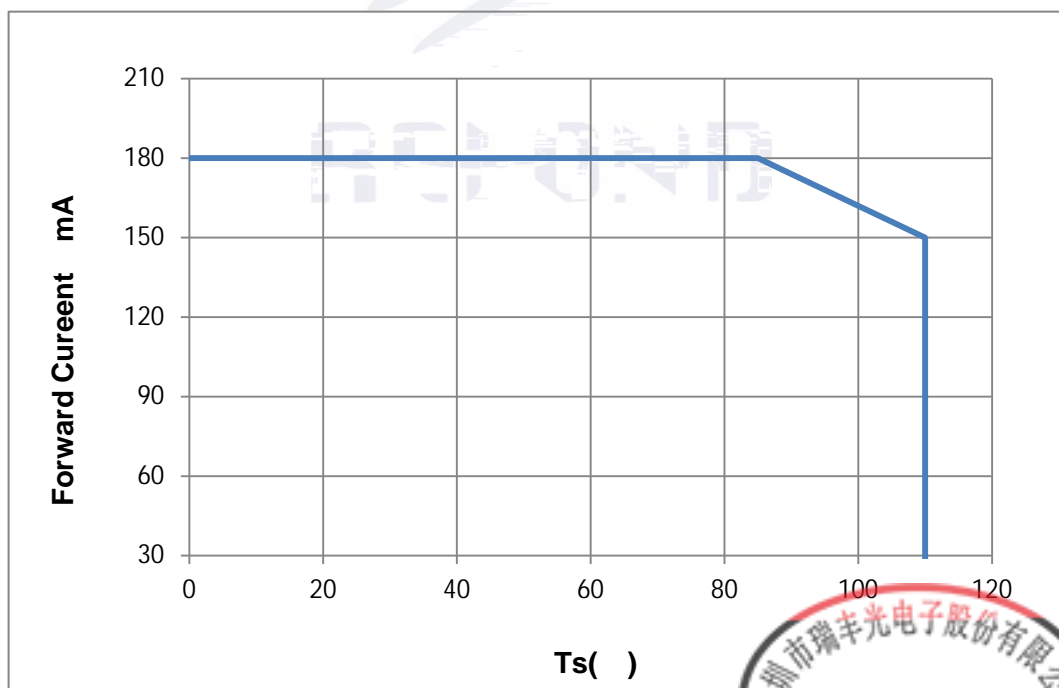


Fig. 1-10 Solder Temperature Vs Forward Current



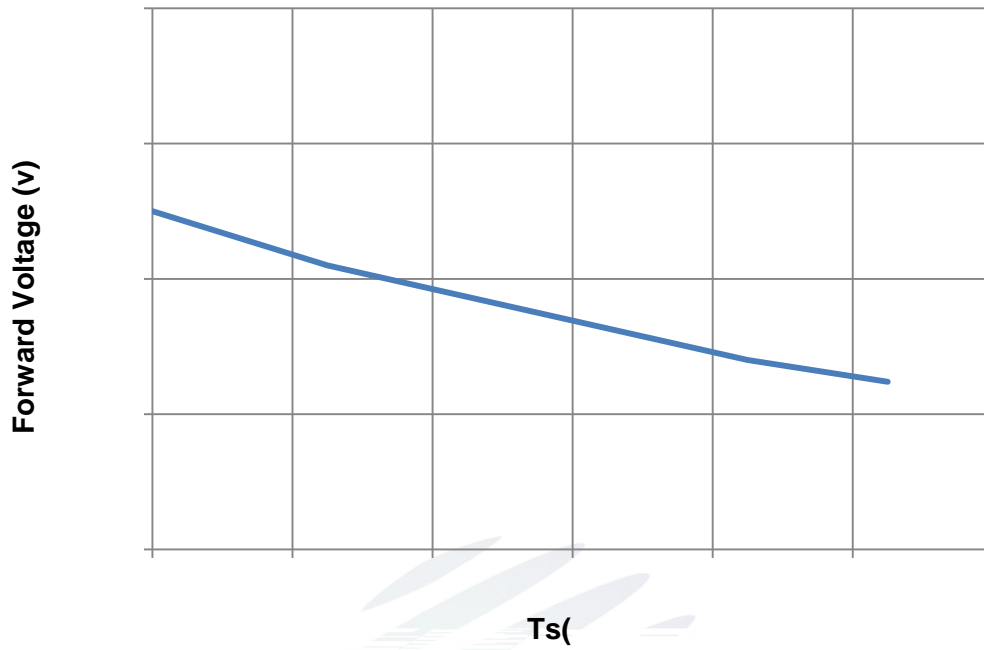


Fig. 1-11 Forward Voltage Vs Solder Temperature

Fig. 1-12 Solder Temperature vs. Color Shift

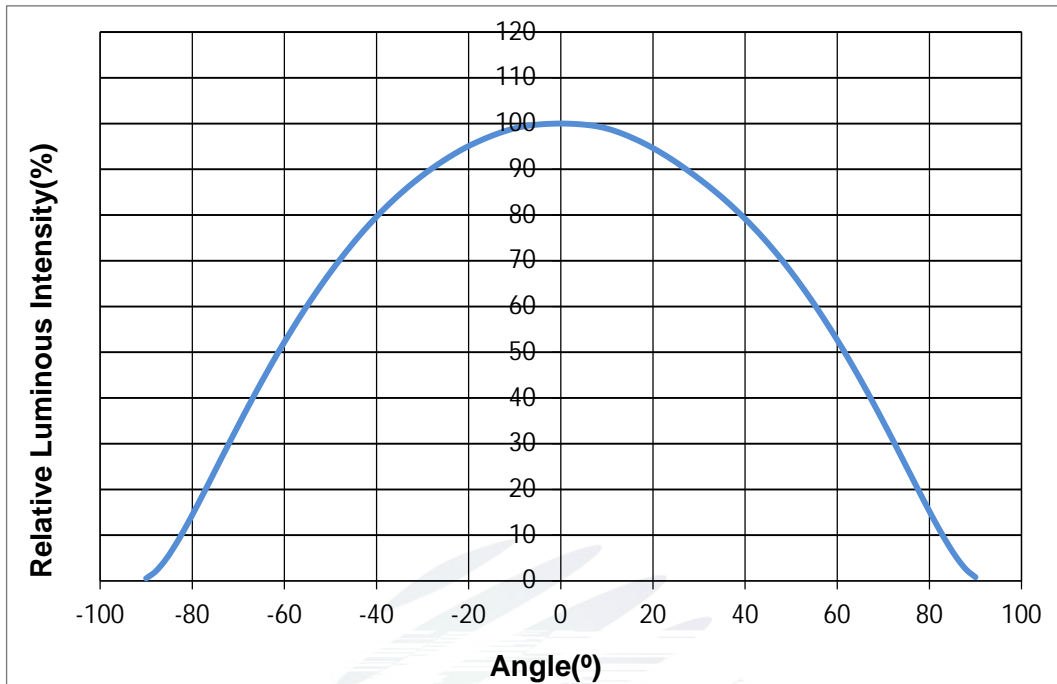


Fig. 1-13 Radiation diagram

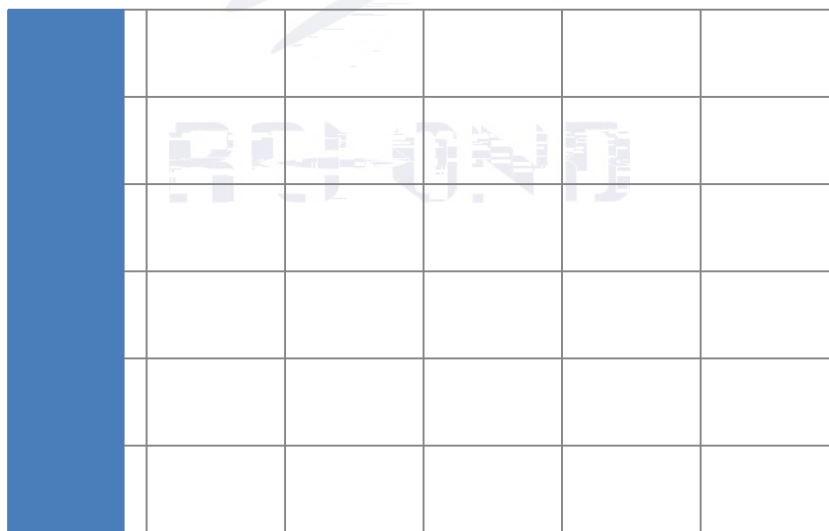


Fig. 1-14 Spectrum Distribution



2. Packaging

2.1 Packaging Specification



Specification

Table 2 Specification

PART NO.	Part Number
SPEC NO.	Spec Number
LOT NO.	Lot Number
BIN CODE	Bin Code
	Luminous flux
XY	Chromaticity Bin
V_F	Forward Voltage
WLD	Wavelength
QTY	Packing Quantity
DATE	Made Date

Fig. 2-3 Label

Moisture Resistant Packing

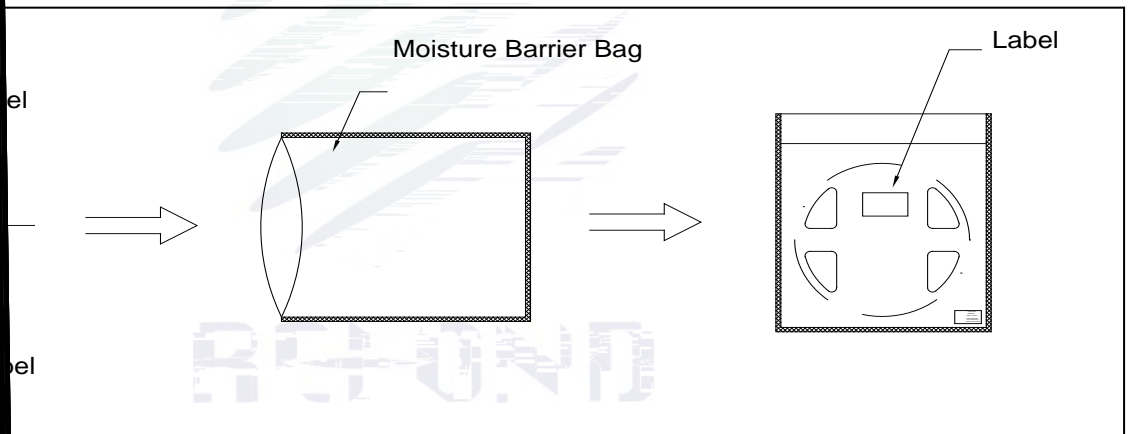


Fig.2-4 Moisture Resistant Packing

Cardboard Box

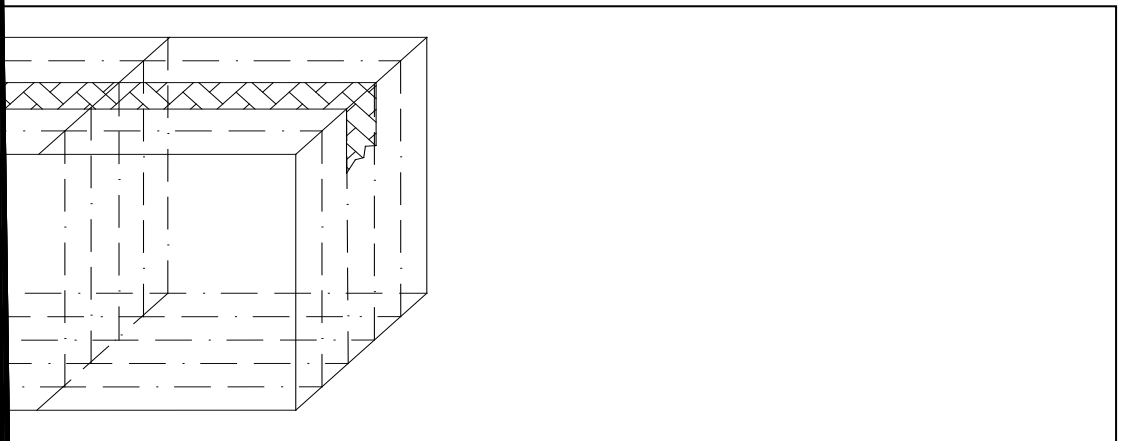


Fig.2- Cardboard Box

2.4 Reliability Test Items And Conditions

Table 2-3 Reliability Test Items And Conditions

Test Items	Ref.Standard	Test Condition	Time	Quantity	Ac/Re /
Reflow	JESD22-B106	Temp:260 max T=10 sec	2times	20pcs.	0/1
MSL2 2	JESD22-A113	85 / 60%RH	168 hrs.	20pcs.	0/1
Thermal Shock	JEITAED-4701 300307	-40 15min 10s 125 15min	1000 cycle	20pcs.	0/1
Life Test	JESD22-A108	Ta=105 If=150mA	1000hrs.	20pcs.	0/1
High Temperature High Humidity Life Test	JESD22-A101	85 / 85%RH If=150mA	1000hrs.	20pcs.	0/1



3. SMT Reflow Soldering Instructions SMT

3.1 SMT Reflow Soldering Instructions SMT

Fig.3-1 SMT Reflow Soldering Instructions SMT

Table 3-1 Reflow parameters

Average temperature rise speed	T _{max}	T _P	3 °C/	Max 3 °C/ s
Preheating: minimum temperature	(T _{min})		150 °C	

4. Handling Precautions

4.1 Handling Precautions

(1) LED operating environment and sulfur element composition cannot be over 100PPM in the LED mating usage material. This is provided for informational purposes only and is not a warranty or endorsement. LED

LED

(2) In order to prevent ex-ternal material from getting into the inside of LED, which may cause the malfunction of LED, the single content of Bromine element is required to be less than 900PPM, the single content of Chlorine element is required to be less than 900PPM, the total content of Bromine element and Chlorine element in the external materials of the application products is required to be less than 1500PPM. This is provided for informational purposes only and is not a warranty or endorsement. LED LED

1500PPM.

(3) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues. Refond advises against the use of any chemicals or materials that have been found or are suspected to have an adverse affect on device performance or reliability. To verify compatibility, Refond recommends that all chemicals and materials be tested in the specific application and environment for which they are intended to be used. Attaching LEDs, do not use adhesives that outgas organic vapor. LED

LED

LED

LED

(4) Handle the component along the side surface by using forceps or appropriate tools; Do not directly touch or Handle the silicone lens surface, it may damage the internal circuitry.



Table 4-1 Storage

Conditions		Temperature	Humidity	Time
Storage	Before Opening Aluminum Bag	30	75%	Within 1 Year From Date
	After Opening Aluminum Bag	30	60%	Recommended for use within 24 hours 24
Baking		60±5	-	24hours 24




Version History/

Date	Revisor	Version	Verifier	Remarks
2017/07/15	hao daijian	E0	hangshiming	ew
2022/09/14	Xian Zhou	E2	Zhu Yiming	Template update





REFOND



Declare

This specification is written both in English and in Chinese and the latter is formal.